

FIG. 1

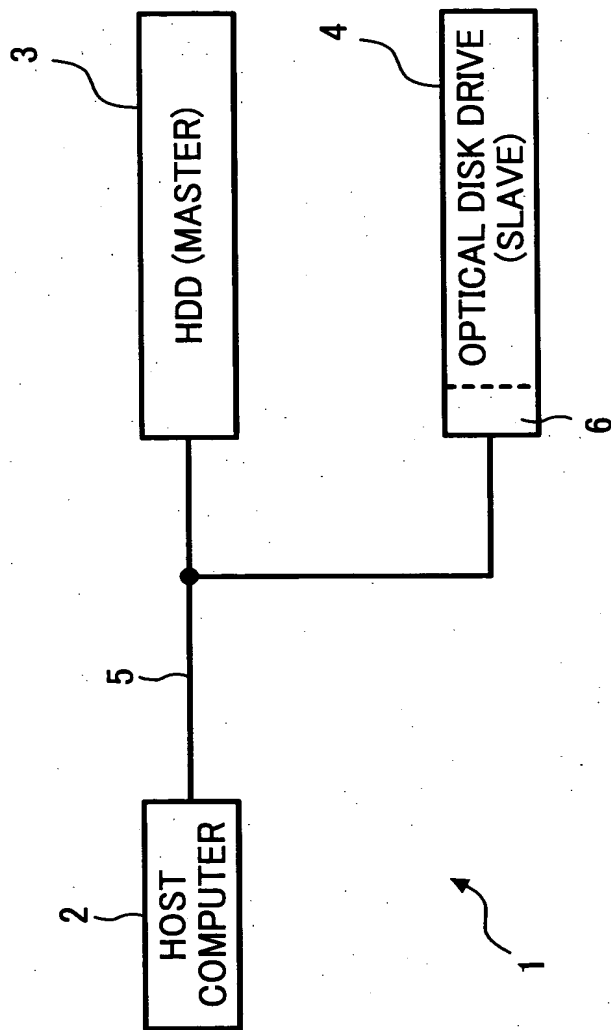


FIG. 2

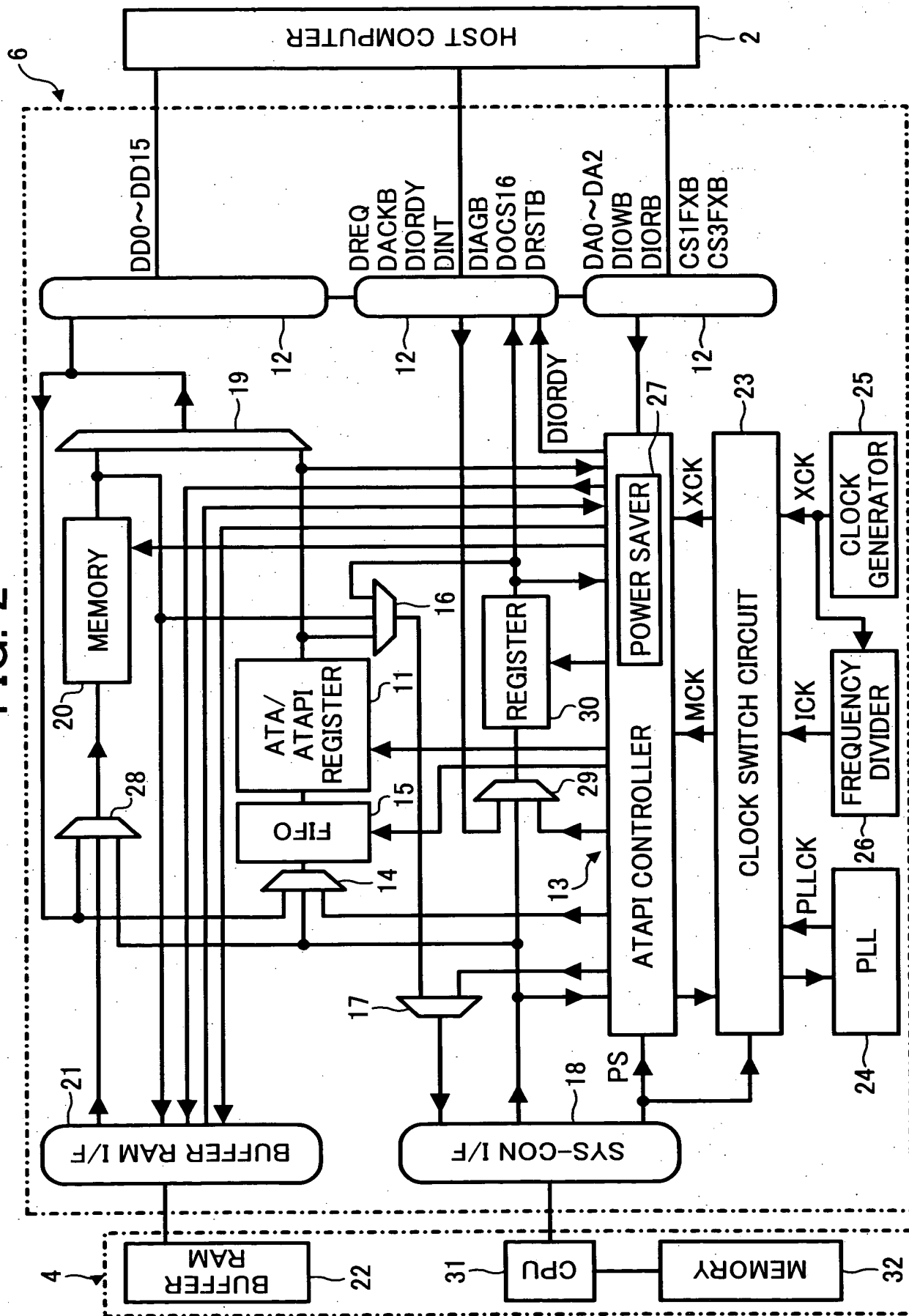


FIG. 3

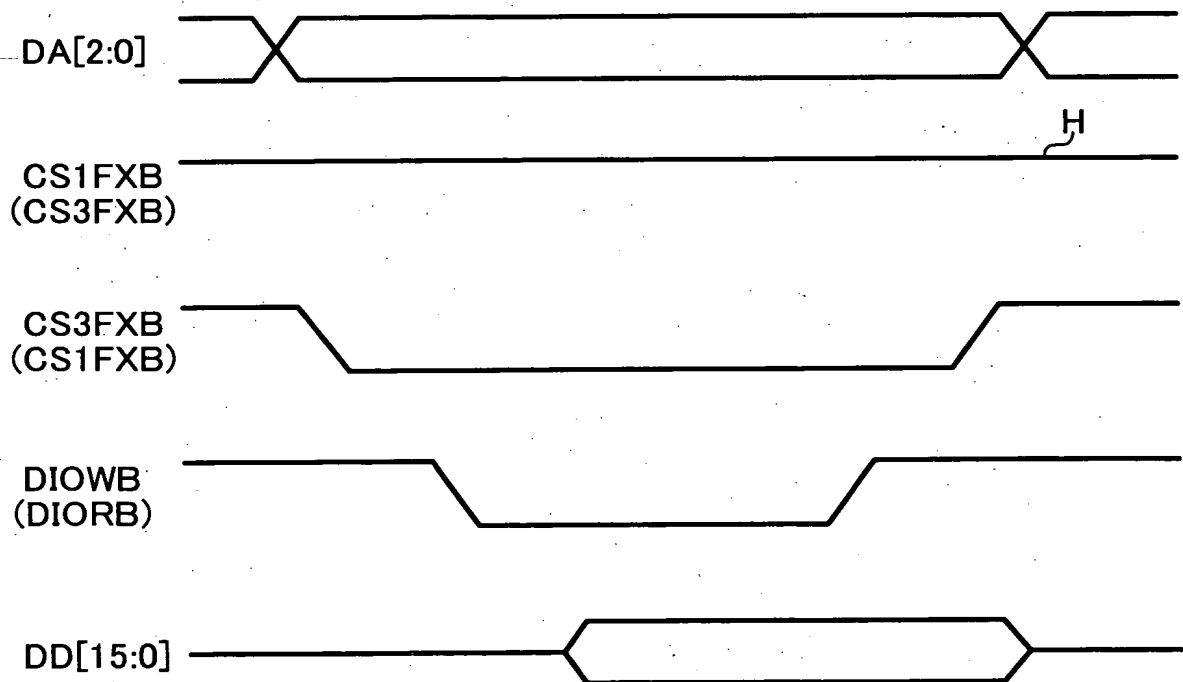


FIG. 4

ADDRESSES					REGISTERS	
CS1FXB	CS3FXB	DA2	DA1	DA0	READ	WRITE
COMMAND BLOCK REGISTER (8/16 BITS)						
0	1		0H		DATA	DATA
0	1		1H		ERROR	FEATURE
0	1		2H		SECTOR COUNT (INTERRUPT REASON)	SECTOR COUNT (INTERRUPT REASON)
0	1		3H		SECTOR NUMBER (RESERVED FOR SAM TAG)	SECTOR NUMBER (RESERVED FOR SAM TAG)
0	1		4H		CYLINDER LOW (BYTE COUNT LOW)	CYLINDER LOW (BYTE COUNT LOW)
0	1		5H		CYLINDER HIGH (BYTE COUNT HIGH)	CYLINDER HIGH (BYTE COUNT HIGH)
0	1		6H		DEVICE/HEAD (DRIVE SELECT)	DEVICE/HEAD (DRIVE SELECT)
0	1		7H		STATUS	COMMAND
CONTROL BLOCK REGISTER (8 BITS)						
1	0		6H		ALTERNATE STATUS	DEVICE CONTROL

FIG. 5

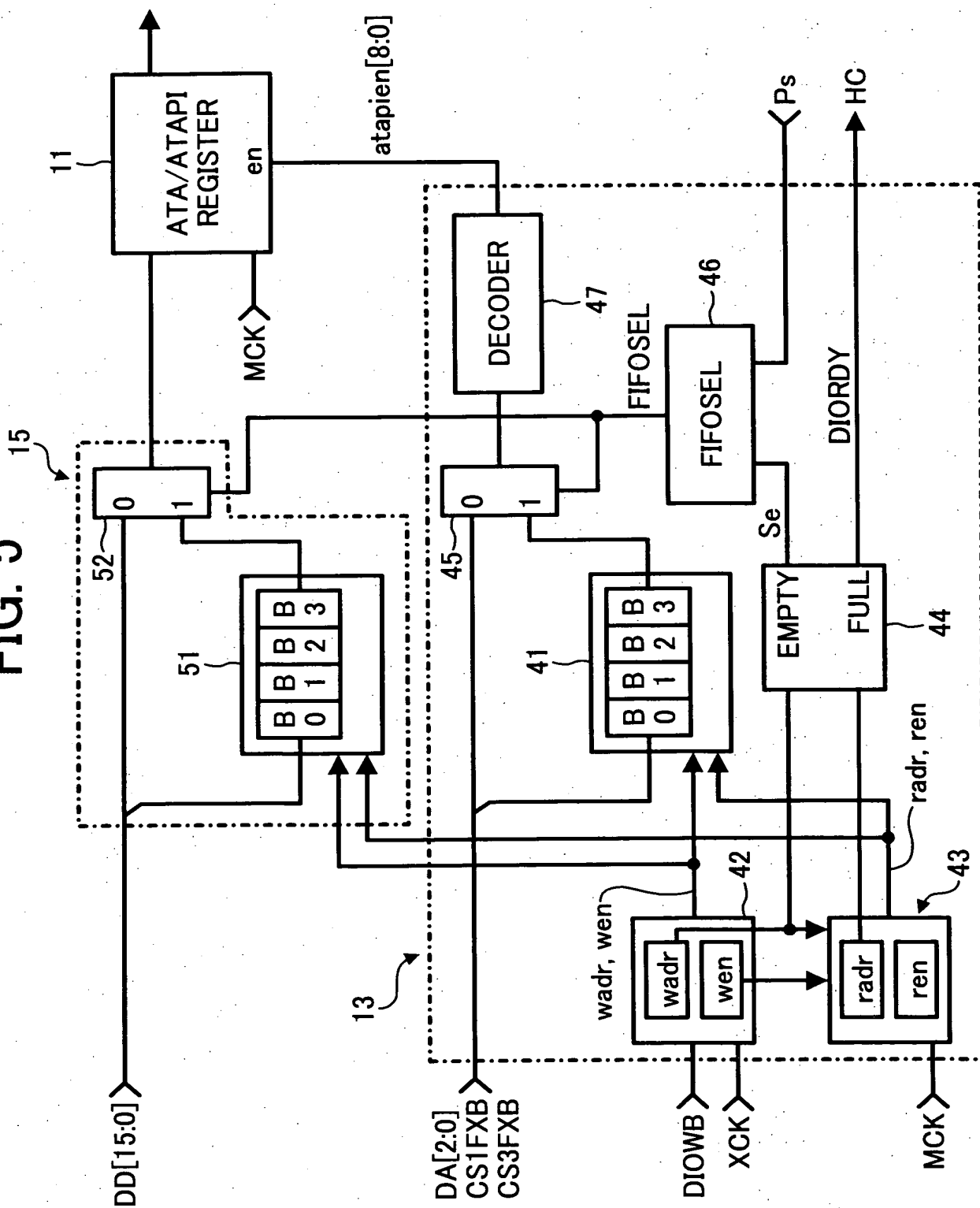


FIG. 6

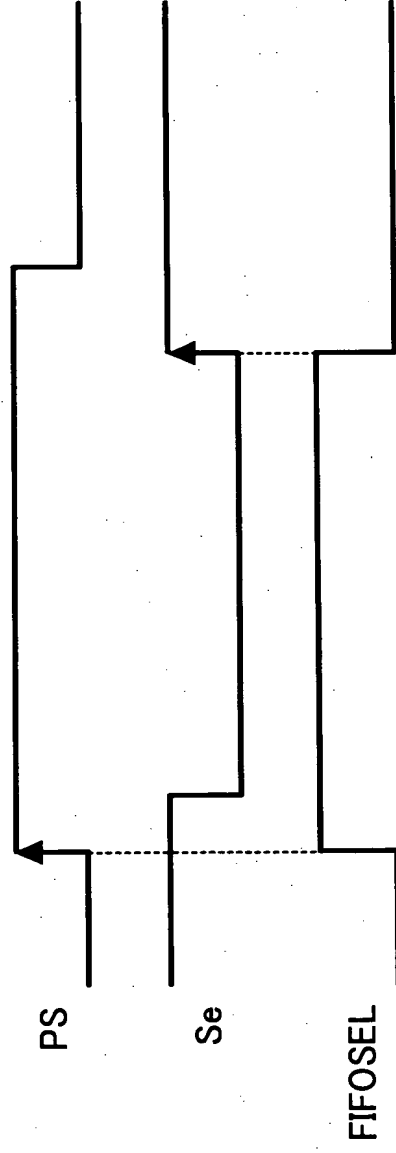


FIG. 7

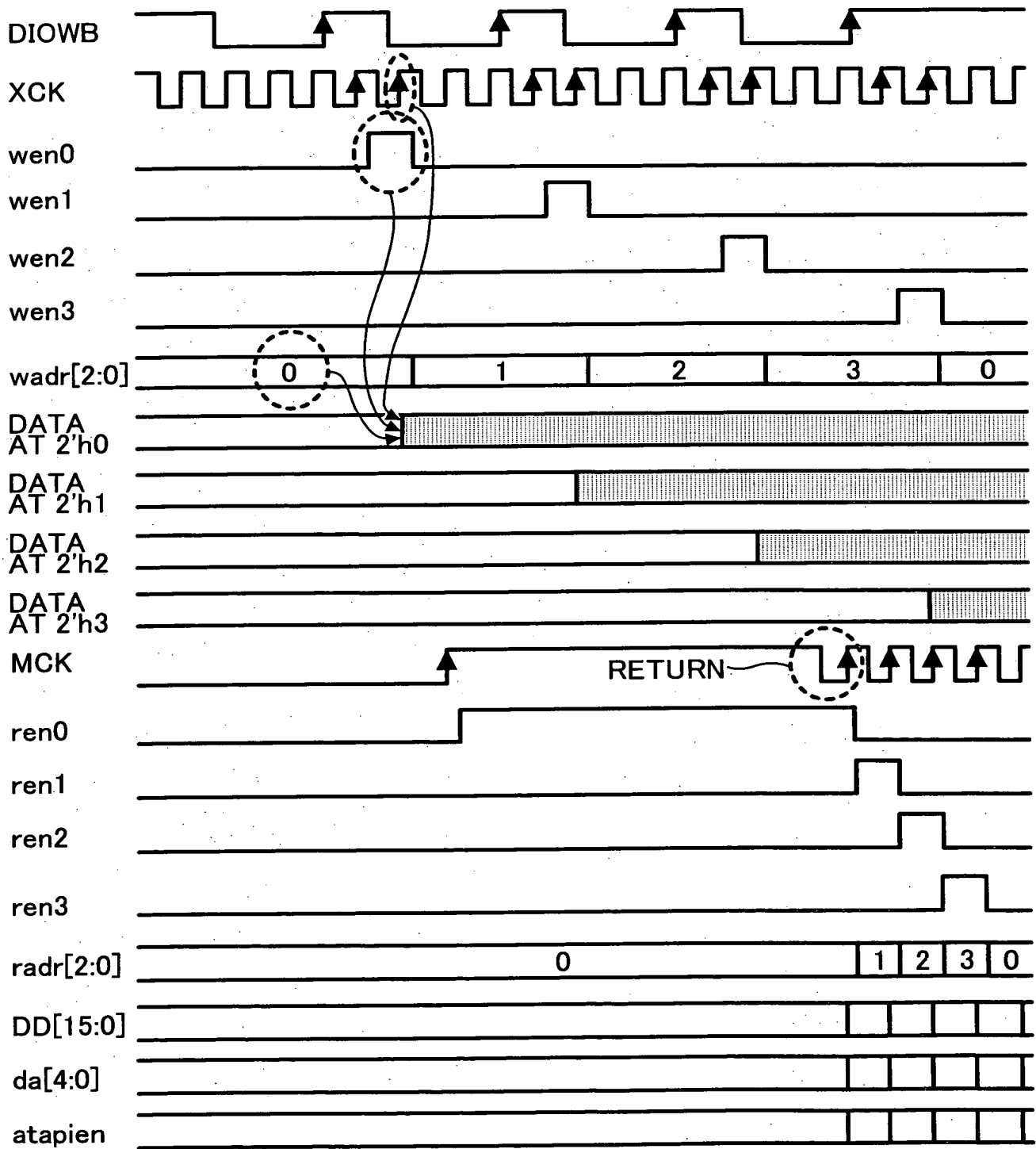


FIG. 8

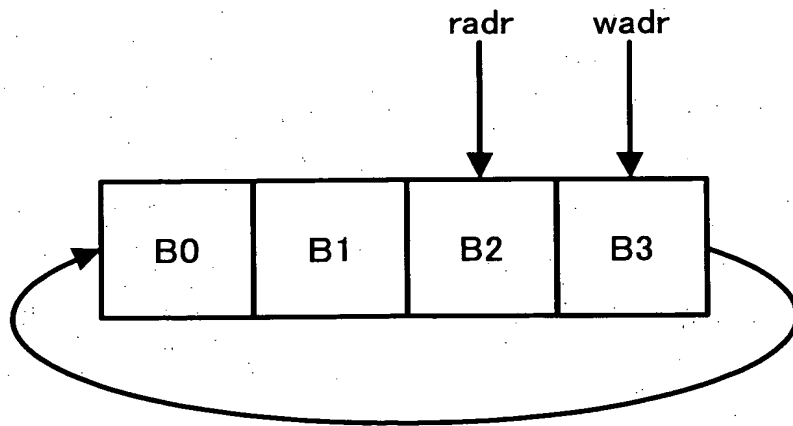


FIG. 9

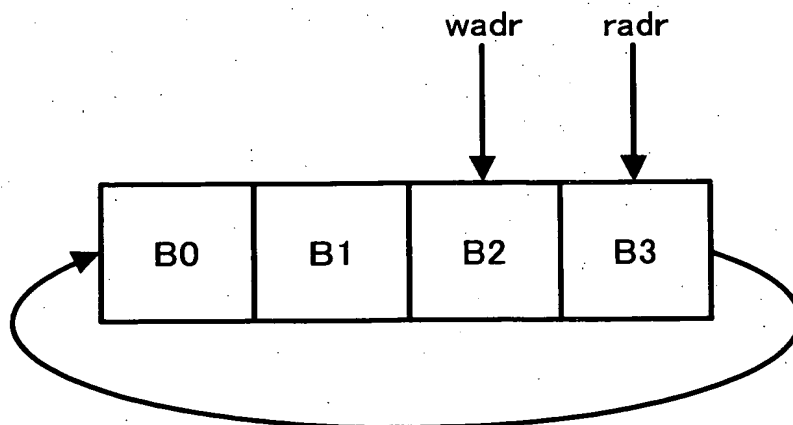


FIG. 10

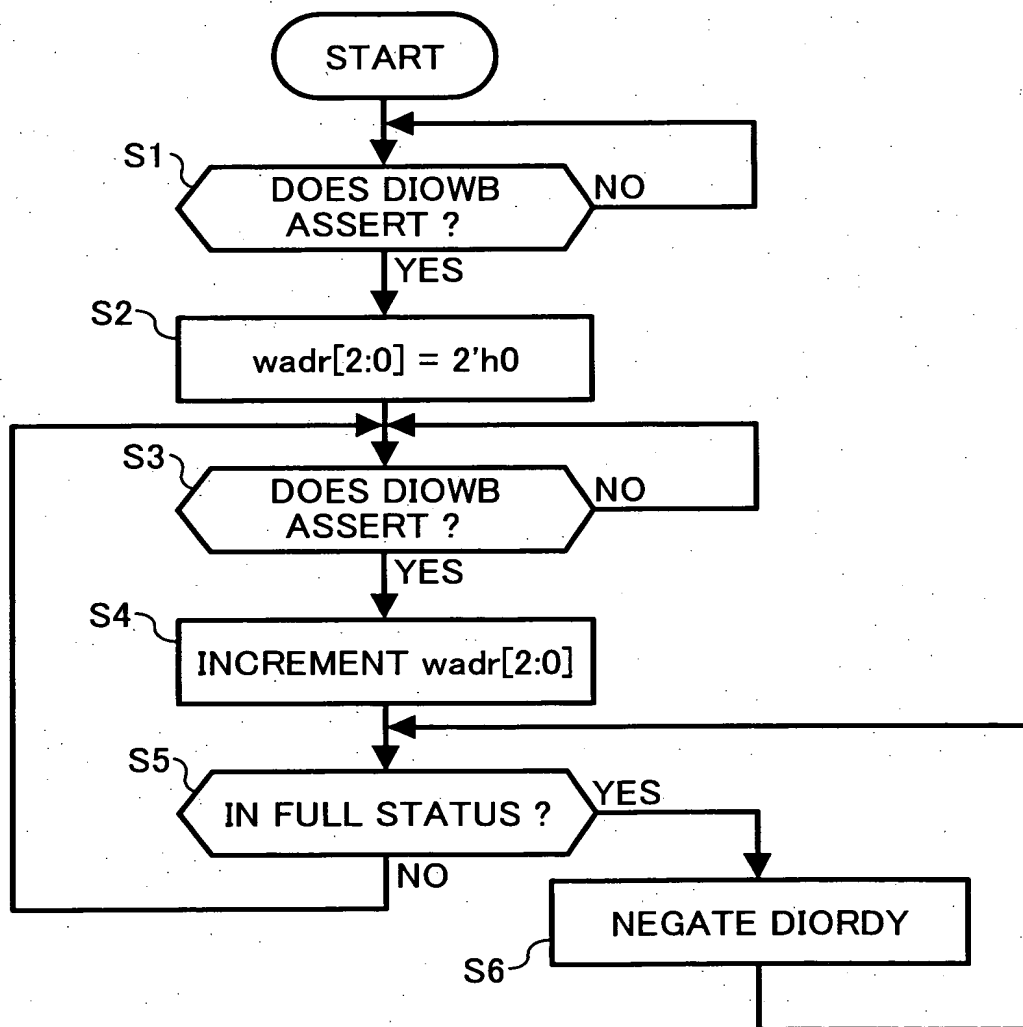


FIG. 11

